



Subst. Form PTO-1449 APPLICANT'S(S') INFORMATION DISCLOSURE STATEMENT	Atty. Docket No.: HIT 2 010-1-1	Serial No.: UNKNOWN
	Applicant(s): Naoki Mitsuishi et al.	
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U.S. PATENT DOCUMENTS

Initial*		Document No.	Date	Name	Class	Subcl.	Filing Date
	AA	4,128,900	12/1978	Lappington			
	AB	4,211,919	7/1981	Ugon			
	AC	4,279,024	7/1981	Schrenk			
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	AE	4,377,857	3/1983	Tickle			
	AF	4,382,279	5/1983	Ugon			
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	AH	4,575,621	3/1986	Dreifus			
	AI	4,605,844	8/1986	Haggan			
	AJ	4,663,741	5/1987	Reinschmidt et al.			
	AK	4,684,791	8/1987	Bito			
	AL	4,698,750	10/1987	Wilkie et al.			
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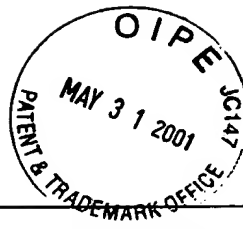
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		Document No.	Date	Country	Class	Subcl.	Translation?
	AT	19665	2/1981	Japan			Yes
	AU	2 079 996	1/1982	Great Britain			English text
	AV	0 081 873	6/1983	Europe			No
	AW	28 40 305	3/1980	Germany			Yes
	AX	2 430 065	1/1980	France			No

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AY	"HITACHI MICROCOMPUTER DATA BOOK, 8-Bit Single Chip", pp. 823-865 (1984).
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	AZ	"THE HITACHI HYORON", vol. 68, no. 7, pp. 29-32 (1986). (w/translation)
	BA	Motorola Microcomputers, Series C, Austin, Texas, Motorola, 1984, pp. (3-827) - (3-850).
	BB	L.L. Goss, "Single chip microcomputer with EEROM allows flexible system design", 1983, WESCOM 83, vol. 27, 34/2, pp. 1-4.
	BC	F. Gruppuso, et al., "On-chip EEPROM holds changing register data for 8-bit microcomputer", December 1984, ELECTRONIC DESIGN, vol. 32, no. 25, pp. 201-209.
	BD	Electronique Industrielles, "Programmeur de memoires PROM utilisant un système SBC 80," October 1979, no. 274, pp. 61-63. (w/translation)
	BE	Wescon Technical Papers, "High Density EPROMs, " 1984, vol. 29, no. 1, pp. 1-9.
	BF	"Electrically erasable memories apply for system work", ELECTRONIC DESIGN, October 13, 1983, pp. 145-148 and 150.
	BG	John Uffenbeck, "Microcomputers and Microprocessors: The 8080, 8085, and L-80 Programming Interfacing and Troubleshooting", 1985, pp. 194-195.
	BH	"Single-Chip Microcomputer", Motorola, Inc., 1984, pp. (3-1033) - (3-1058).
	BI	Electronics Week, "Memories, One-Megabit EPROMs Invade Disk Territory", April 22, 1985, pp. 52-54.
	BJ	Nikkei Electronics, "Large Capacity EPROM - Its Various Direction Great Change Since 1M", August 12, 1985, pp. 119-137.
	BK	Andrew M. Veronis, "Memory", in Andrew M. Veronis, Microprocessing Hardware and Applications (Reston Virginia, Prentice-Hall Company (1984), pp. 82.
	BK	French Search Report dated July 17, 1992

Examiner:

Date Considered:

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if in conformance and not considered. Include copy of this form with next communication to applicant.